13/A EWm 11-12-03

Docket No. 42390P10071

<u>PATENT</u>



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Terry L. Kendall, et al.

Application No. 09/752,594

Filed: December 27, 2000

For: Method and Apparatus Including Special)

Programming Mode Circuitry Which
Disables Internal Program Verification
)

Operations By A Memory (As Amended))

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450 Examiner: Gossage, Glana CEIVED

Art Unit: 2187

OCT 2 3 2003

Technology Center 2100

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450

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Date of Deposit,

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Date

AMENDMENT

Sir:

In response to the Office Action dated July 2, 2003, please amend the above-identified application as follows:

Amendments to the Title begin on page 2 of this paper.

Amendments to the Abstract begin on page 3 of this paper.

Amendments to the Specification begin on page 4 of this paper.

Amendments to the Drawings begin on page 10 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Amendments to the Claims are reflected in the listing of claims which begins on page 11 of this paper.

Remarks/Arguments begin on page 17 of this paper.

An Appendix including amended drawing figures is attached following page 23 of this paper.

IN THE TITLE

Please delete the present Title and substitute the following title:



Method and Apparatus Including Special Programming Mode Circuitry Which

Disables Internal Program Verification Operations By A Memory.